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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Tsuneo KONISHI, et al.

Docket No: Q68160

Appln. No.: 10/050,972

Group Art Unit: 2833

Confirmation No.: 7887

Examiner: Felix O. Figueroa

Filed: January 22, 2002

For: METHOD FOR MANUFACTURING PLASMA DISPLAY PANEL

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated April 9, 2003, please amend the above-identified application as follows:

IN THE SPECIFICATION:

In order to form the rear substrate 12, data electrodes are formed on a glass substrate for the rear substrate (Step S6). Thereafter, a white reflective layer (dielectric layer) is formed on the whole surface (Step S7). Then, ribs 16 are formed to partition discharge cells (Step S8). A gap (discharge space) between the front and rear substrates is secured by the ribs 16. Thereafter, a phosphor that emits predetermined color light is applied onto side faces of ribs 16 and onto an exposed surface of the white reflective layer (Step S9). Thereafter, a glass frit (sealing frit) 13 made of, for example, amorphous glass with a low softening temperature is applied onto the edge of a display screen by use of a dispenser (Step S10). When the glass frit 13 is applied, a glass frit